

Suman Navaratnarajah

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🐙 github.com/suman1209

CAREER OBJECTIVE

- I am very much interested and looking forward to involve in **Real World Projects**.
- I want to work with a team where I can apply my knowledge and gain practical and professional experience.
- **Currently seeking internship in the field of my interest** – Robotics and AI, Machine learning, Computer Vision, Embedded systems, Processor Design.

EDUCATION

University of Moratuwa

BSc. Eng (Hons.)

Electronics and Telecommunication Engineering

- Cumulative GPA – **3.45/4.2**

Jaffna Hindu College

G.C.E Advanced Level Examination

- **3A's** (Combined Mathematics, Physics, Chemistry).

Manipay Angel International School

G.C.E Ordinary Level Examination

- **6A's** (Including Information Communication Technology, German language, English Literature).

Moratuwa, SriLanka

Oct 2016 – Present

Jaffna, SriLanka

May 2013 – Sep 2015

Manipay, SriLanka

Jan 2010 – Dec 2012

MOOC

- Machine Learning (Coursera)
- Data structures and Algorithm (Coursera)
- Introduction to FPGA design for Embedded Systems. (Coursera)
- Fundamentals of Digital Image and Video processing (Coursera)

ADVANCED TECHNICAL SKILLS & KNOWLEDGE AREAS

- Programming Language –**Python** [good] ,**C#**,**C++** [Basic]
- **MATLAB**
- **SolidWorks** [good]
- **OpenCV python**, [in Anaconda and Jupyter Lab]

PROFESSIONAL QUALIFICATION

- **Data structures and Algorithm**
- **PCB designing** in Altium and making complete circuit
- **Digital circuit design** (tools – DE 2-115 FPGA,Verilog , Quartus , Modelsim Altera)
- **Knowledge of processor architecture**
- Fundamentals of Image processing
- Familiarity with microcontrollers.

LANGUAGE

- English [fluent]
- Deutsch [good]
- Tamil [fluent]

AWARDS AND COMPETITIONS

Intra University Robotics Competition	2017,2018
○ Built a four wheeled robot for a given task	
Asian Finalists in IEEE SS12	2018
○ Innovation Challenge	
Finalists in Yarl Geek Challenge	2018
○ Start -up Competition	
IEEE Electronic Design and Competition	2018
○ Built a DC to DC Voltage regulator	

PROJECTS

For detailed description of all my projects, refer the link below.

🔗 <https://sumanrbt1997.wixsite.com/suman>

Designing and implementing a processor for down sampling an image	Ongoing
○ FPGA [DE2-115], Quartus prime lite, Verilog	
UART Transceiver on FPGA	2018
○ Verilog, FPGA	
○ Programming and testing of UART communication protocol on FPGA [DE2-115].	
Created a four wheeled Robot for a given Task	2018
○ Arduino microcontroller	
Worked for a wall – maze Solving Robot	2018
○ Flood filling Algorithm	
Household Door security system	2017
○ pic 16F877A & laser cut perspex enclosure	

LEADERSHIP ACTIVITIES

- Led the team which conducted Education Ministry Sponsored **Robotics Workshop** in the **Northern Province** (2018)
- **Department representative** for semesters 4 and 5 [Jun 2018 – Jun 2019]
- **Taught CIE A/L physics and been the Lab exam supervisor for CIE (2016)**
Centre – Angel International School Manipay [Aug 2015 – Aug 2016]

NON - RELATED REFERENCES

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